

Transistor :-

↳ Transfer resistor

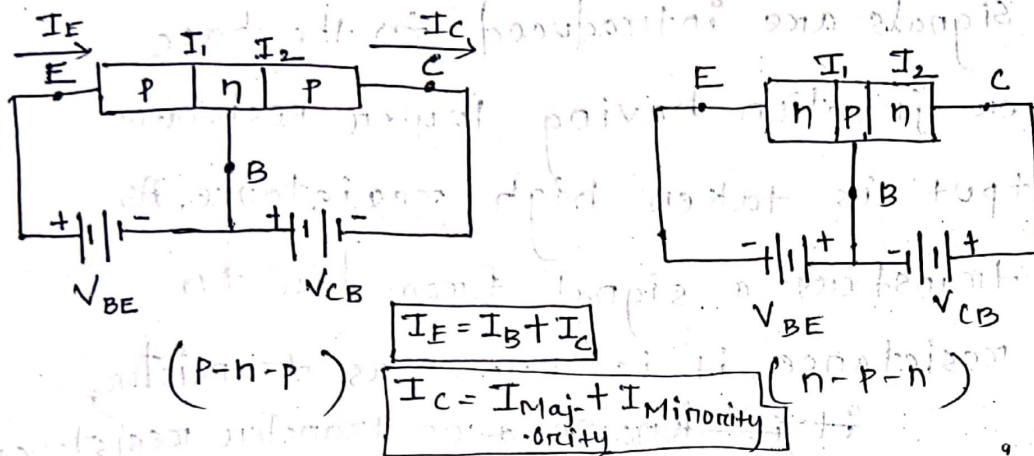
Two types :- (1) n-p-n transistor
(2) p-n-p transistor

Three terminals of transistor :-

→ Emitter

→ Base

→ ~~Collection~~ collector.



Width: $C > E > B$

Doping: $E > C > B$

I_1 = Base emitter junction (Forward biased)

I_2 = collector base junction (Reverse biased)

→ In BJT (Bipolar junction transistor) both e^- & holes are responsible for conduction.

∴ It is called as bipolar junction transistor.

→ Here base is thinner than collector & emitter. Emitter is heavily doped so that it can inject a large no. of charge carriers i.e. e^- or holes into the base.

→ Base is slightly doped & very thin.

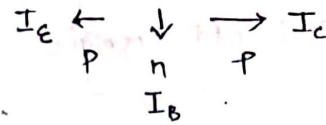
So, it passes most of the emitter injected

- charge carriers to the collector.
- Collector is moderately doped.
 - Size & width is max^m, bcoz it has to collect the e^- .
 - For transistor operation, the emitter based junction is always forward biased & collector based junction is always reverse biased.
 - Weak signals are introduced in the base emitter junction having lower resistance & output is taken high resistance. As BJT transfers a signal from low to high resistance, it is known as transistor, ~~it is known as~~ or transfer resistance.
 - BJT generally used in amplification of weak signals & in switching operations.
 - $I_E = I_B + I_C$
 - $I_C = I_{\text{Majority}} + I_{\text{Minority}}$
 - $I_C = I_M + I_{C0}$
 - I_{C0} = Collector current with emitter open.
 - I_E & I_C is in mA range.
 - I_B is in μA range.
 - Types of transistor configuration:-
 - (1) Common base (CB):-
 - (2) Common Emitter (CE):-
 - (3) Common collector (CC):-

COMMON BASE :-

19/09/19

$$I_E = I_B + I_C$$



$$I_C = I_{C_{maj}} + I_{C_{min}}$$

$$I_C \cong I_{C_{maj}}$$

$$I_C = I_{C_{maj}} + I_{CBO} \quad [\text{When input side is open}]$$

Here; $\alpha = \frac{I_C}{I_E}$

$$\Rightarrow I_{C_{maj}} = \alpha I_E$$

$$\text{So, } I_C = \alpha I_E + I_{CBO}$$

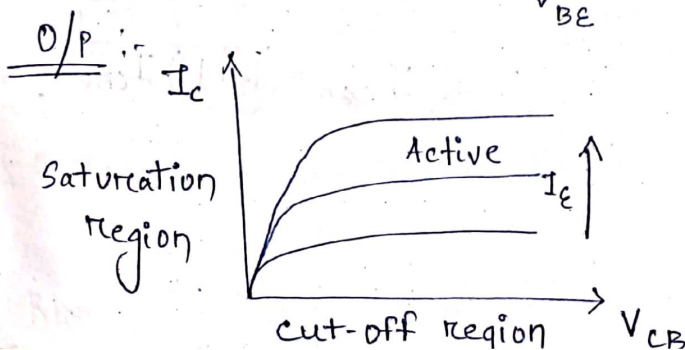
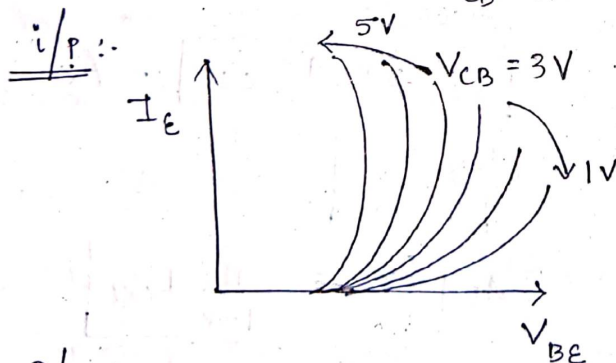
$$\Rightarrow I_C = \alpha (I_C + I_B) + I_{CBO}$$

$$\Rightarrow I_C = \alpha I_C + \alpha I_B + I_{CBO}$$

$$\Rightarrow I_C (1 - \alpha) = \alpha I_B + I_{CBO}$$

$$\Rightarrow I_C = \frac{\alpha}{1 - \alpha} I_B + \frac{1}{1 - \alpha} I_{CBO} \quad \text{--- (1)}$$

$$V_{CB} = 5V$$

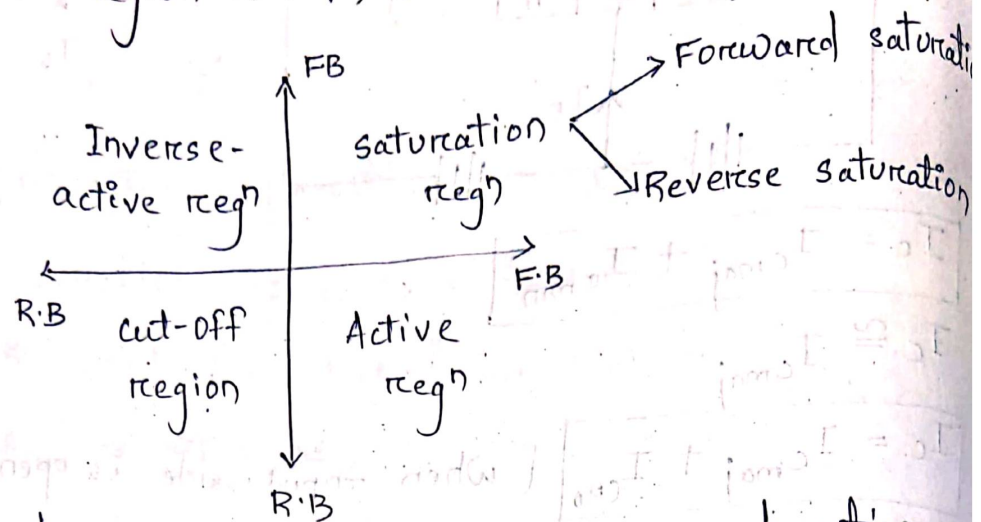


$I_E = \text{Constant}$
Output

Saturation region $\rightarrow J_i, J_o = F \cdot B$

Active region $\rightarrow J_i = F \cdot B, J_o = R \cdot B$

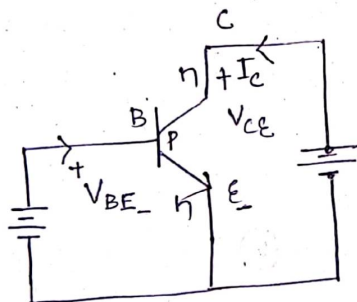
Cut-off region $\rightarrow J_i, J_o = R \cdot B$



Forward saturation;
 $i_o = I_e \uparrow + I_c \rightarrow 0$
 when $V_{BE} > V_{BC}$
 $J_i > J_o$

Reverse saturation;
 $i_o = I_c \uparrow + I_e \rightarrow 0$
 when $V_{BC} > V_{BE}$
 $J_o > J_i$

COMMON EMITTER :-



$$\beta = \frac{I_c}{I_B} = \frac{I_c / I_e}{I_e / -I_c / I_e} = \frac{\alpha}{1-\alpha}$$

$$I_{c_{maj}} = \beta I_B$$

When it opens,

$$I_{CEO}$$

$$I_c = \beta I_B + I_{CEO} \quad (2)$$

$$\beta = \frac{\alpha}{1-\alpha}$$

$$\text{And } I_{CEO} = \frac{1}{1-\alpha} I_{CBO}$$

$$\Rightarrow I_{CEO} = (\beta + 1) I_{CBO}$$

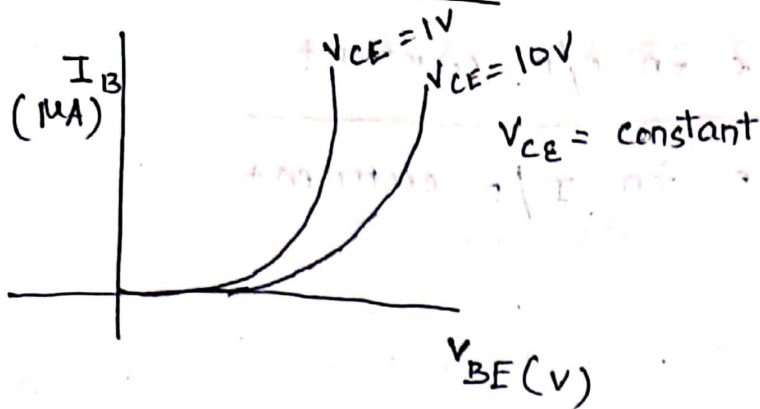
$$\therefore \beta = \frac{\alpha}{1-\alpha}$$

$$\Rightarrow (\beta + 1) = \frac{\alpha}{1-\alpha} + 1$$

$$\Rightarrow (\beta + 1) = \frac{1}{1-\alpha}$$

Input V-I characteristic :-

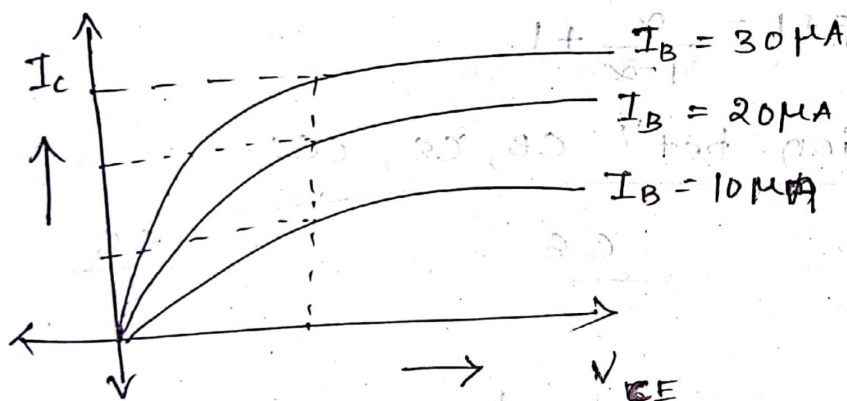
23/09/2019



- Input V-I characteristic curve is curve betⁿ i/p volt current I_B & o/p voltage V_{CE} keeping V_{CE} constant.
- By increasing V_{CE} , V_{BE} is increasing.

Output V-I characteristic :-

- It is the characteristic betⁿ o/p voltage V_{CE} & output current I_{CE} keeping input current I_B constant.



- By increasing I_B , I_C current is increasing also.
- In o/p characteristics, the bend shape is because of early effect.
- Common Collector (CC collⁿ):

Amplification factor:-

$$\gamma = \frac{\text{change in o/p current}}{\text{change in I/p current}}$$

$$= \frac{\Delta I_E}{\Delta I_B}$$

$$= \frac{\Delta (I_C + I_B)}{\Delta I_B}$$

$$= \frac{\Delta I_C + \Delta I_B}{\Delta I_B}$$

$$= \frac{\Delta I_C}{\Delta I_B} + 1$$

$$\Rightarrow \boxed{\gamma = \beta + 1}$$

$$\therefore \gamma = \beta + 1 = \frac{\alpha}{1-\alpha} + 1$$

Comparison betⁿ CB, CE, CC:-

	<u>CB</u>	<u>CE</u>	<u>CC</u>
I/p impedance:-	Low	moderate	high
O/p impedance:-	High	moderate	low
voltage gain:-	High	moderate	low
Current gain:-	Low	moderate	high
power gain:-	No	moderate	No

→ Out of this 3 configuration, CE is mostly used.
 Bcoz, CE has both voltage as well as current gain.

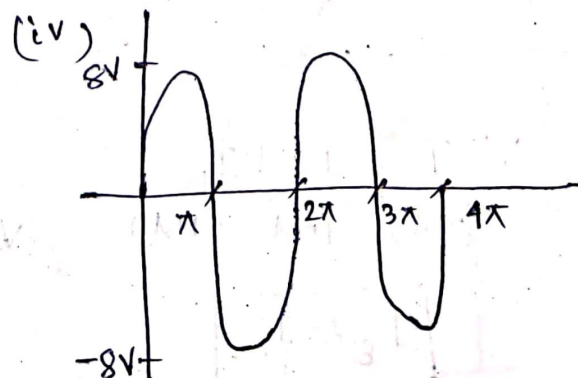
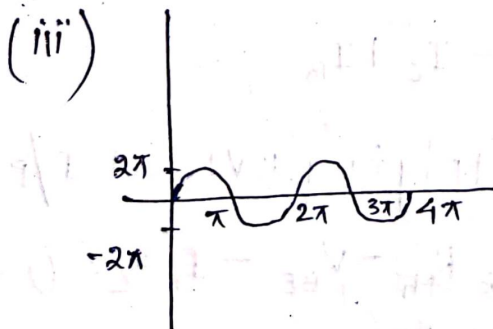
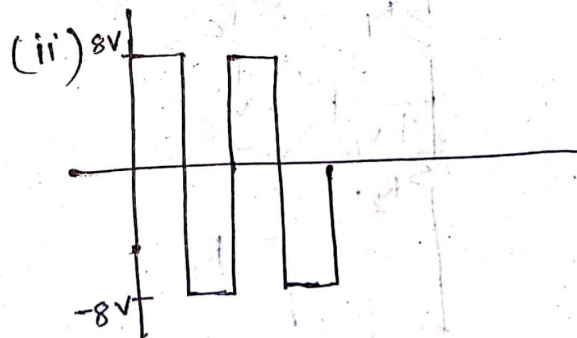
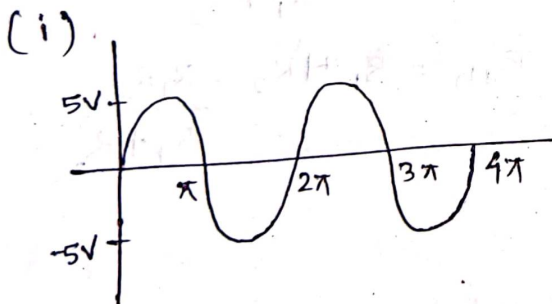
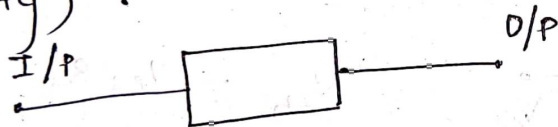
→ Any amplifier having both current & voltage gain. It is ^{having} called power gain.

Region of operation:-

	J_E	J_C
	Forward bias	Reverse bias.
① Active (Amplifier)	→	→ FB
② Saturation (ON switch)	→ FB	→ RB
③ Cut off (OFF switch)	→ RB	→ FB
④ Inverse active (Attenuation)	→ RB	→

Biasing in BJT :-

→ Because, condⁿ is due to both e^- & holes. (both polarity).



→ For faithful amplification o/p of amplifier should be exact replica reproduction of i/p and input should be multiplied by factor, which is greater than 1. which is the output.

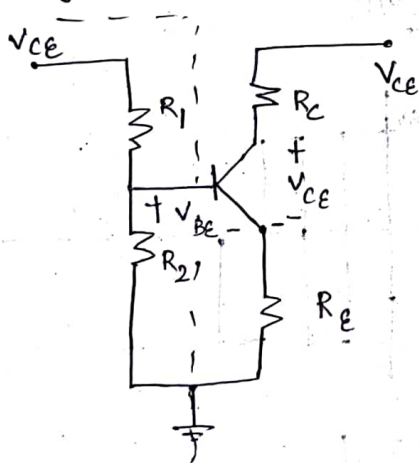
Ex:- In 3rd case (iii) exact replica reproduction
→ For faithful amplification, the operating point should be at the middle of ~~amp~~ active region.
→ For faithful amplification I_C (dc) should be independent of β .

→ To achieve the above 2 condⁿ we need biasing. There are 2 types of biasing.

(1) Fixed bias

(2) Voltage divider bias.

✓ Voltage divider bias:-



$$V_{th} = \frac{V_{CC} R_2}{R_1 + R_2}$$

$$R_{th} = R_1 || R_2 = \frac{R_1 R_2}{R_1 + R_2}$$

$$I_C = \beta I_B$$

$$I_E = I_C + I_B$$

$$\begin{matrix} I_E = I_C + I_B \\ (mA) \quad (mA) \quad (\mu A) \end{matrix}$$

$$I_E \approx I_C$$

So; Applying KVL, at I/p loop

$$V_{th} - I_B R_{th} - V_{BE} - I_E R_E = 0 \quad \text{--- (1)}$$

$$I_E = I_C + I_B$$

$$= \beta I_B + I_B$$

$$= (\beta + 1) I_B \Rightarrow$$

$$I_B = \frac{I_E}{\beta + 1}$$

eqⁿ (1) can be

$$V_{th} = \frac{I_E R_E}{\beta + 1}$$

$$\Rightarrow V_{th} - V_{BE}$$

$$\Rightarrow I_E =$$

$$I_C =$$

$$R_E$$

$$I_C$$

$$I_C$$

Applying

$$V$$

$$V$$

Appr

eqn ① can be written as;

$$V_{th} = \frac{I_E}{\beta + 1} R_{th} - V_{BE} - I_E R_E = 0$$

$$\Rightarrow V_{th} - V_{BE} = I_E \left(R_E + \frac{R_{th}}{\beta + 1} \right)$$

$$\Rightarrow I_E = \frac{V_{th} - V_{BE}}{R_E + \frac{R_{th}}{\beta + 1}} \approx I_C$$

$$I_C = \frac{V_{CC} - V_{th}}{R_E + \frac{R_{th}}{\beta + 1}}$$

$$R_E \gg \frac{R_{th}}{\beta + 1}$$

$$I_C = \frac{V_{CC} - V_{th}}{R_E}$$

To make $I_C(dc)$ independent of β ;

$$R_E \gg \frac{R_{th}}{\beta + 1}$$

Applying KVL at output loop;

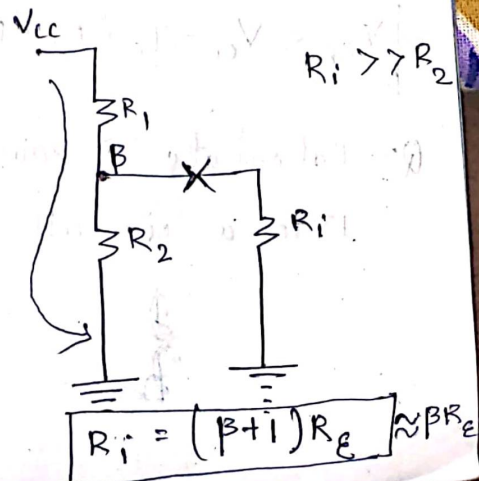
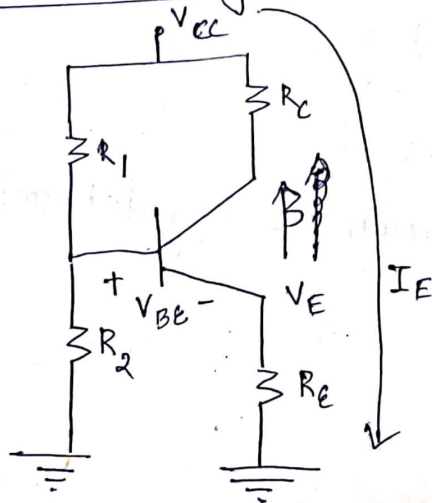
$$V_{CC} - I_C R_C - V_{CE} - I_E R_E = 0$$

$$V_{CE} = V_{CC} - I_C R_C - I_E R_E$$

$$= V_{CC} - I_C R_C - I_C R_E$$

$$V_{CE} = V_{CC} - I_C (R_C + R_E) = V_{CE0} = V_{CE(dc)}$$

Approximate Analysis :-



For good accuracy;

$$R_i \gg 10R_2$$

$$\Rightarrow \beta R_E \gg 10R_2$$

$$V_B = \frac{V_{CC} R_2}{R_1 + R_2}$$

$$V_{BE} = V_B - V_E$$

$$V_E = V_B - V_{BE}$$

$$V_E = \frac{V_{CC} R_2}{R_1 + R_2} - V_{BE}$$

$$I_E = \frac{V_E - 0}{R_E} = \frac{\frac{V_{CC} R_2}{R_1 + R_2} - V_{BE}}{R_E}$$

$$I_E = I_C = \frac{V_{CC} R_2}{R_1 + R_2} - V_{BE}$$

$$I_E = I_C = \frac{V_{CC} R_2}{R_1 + R_2} - V_{BE}$$

$$= I_{CQ} = I_{C(dc)}$$

Applying KVL at O/P :-

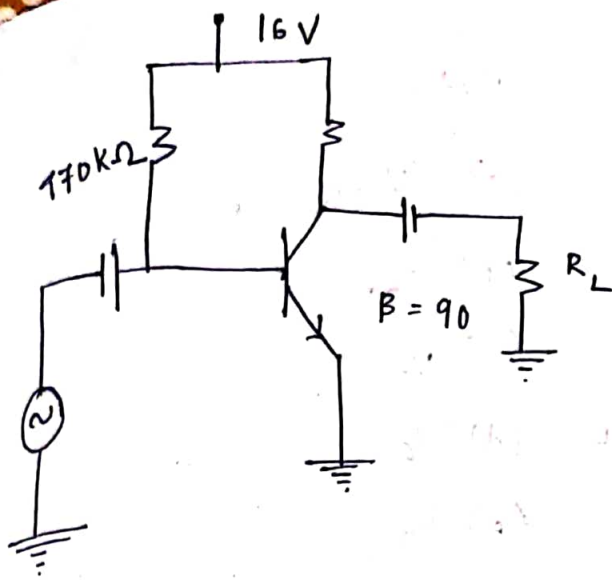
$$V_{CC} - I_C R_C - V_{CE} - I_E R_E = 0$$

$$\Rightarrow V_{CE} = V_{CC} - I_C (R_C + R_E)$$

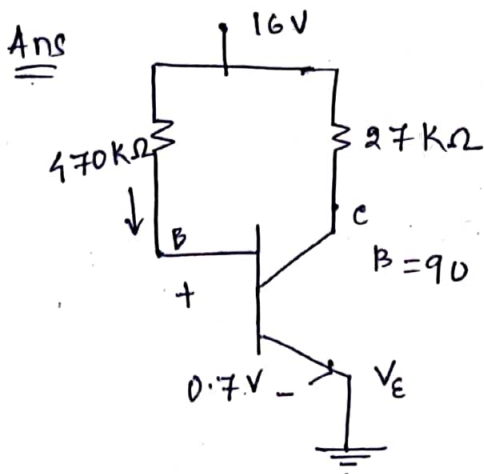
Q:- Calculate Q point.

For a circuit shown in fig. determine.





Determine Q ($V_{CE}(dc)$, $I_C(dc)$), I_{BQ} , V_C , V_B , V_E ?



$$V_{BE} = 0.7V$$

$$\Rightarrow V_B - V_E = 0.7V$$

$$\Rightarrow V_B = 0.7V$$

$$I_B = \frac{16 - 0.7}{470K} = \frac{15.3}{470} \text{ mA}$$

$$\underline{\text{DTC}} \quad 16 - 470K I_B - V_{BE} = 0$$

$$I_B = \dots$$

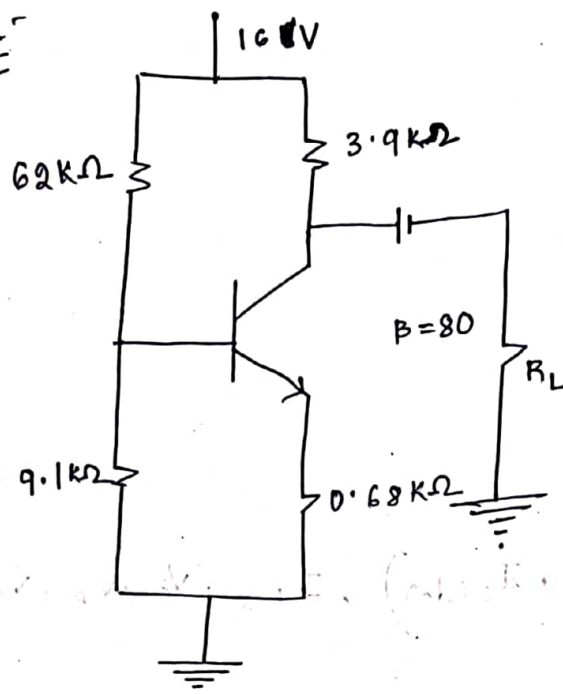
$$\boxed{I_C = \beta I_B}$$

Apply KVL of O/P :-

$$16 - I_C \times 27K = V_{CE} - V_C = 0$$

$$\theta(V_{CE}, I_C)$$

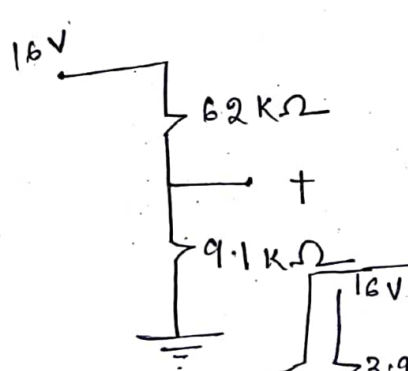
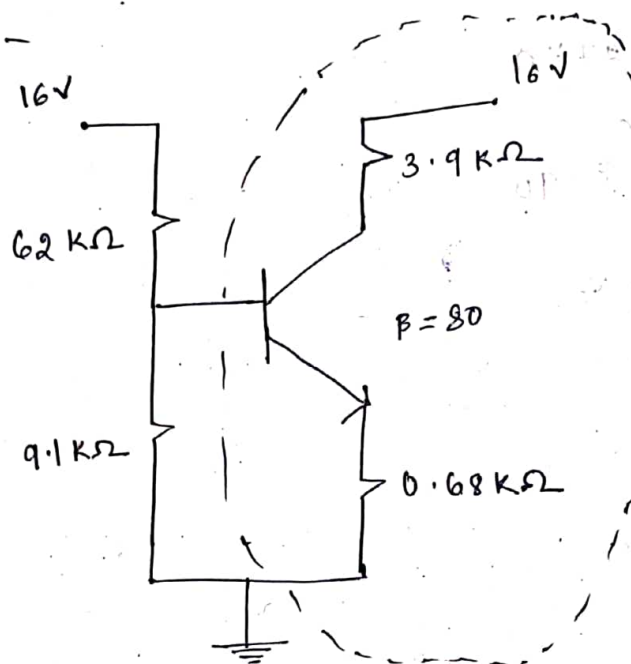
Q:-



Determine

- (a) (V_{CEQ}, I_{CQ})
- (b) I_{BQ}
- (c) V_C
- (d) V_E
- (e) V_B

Ans :-

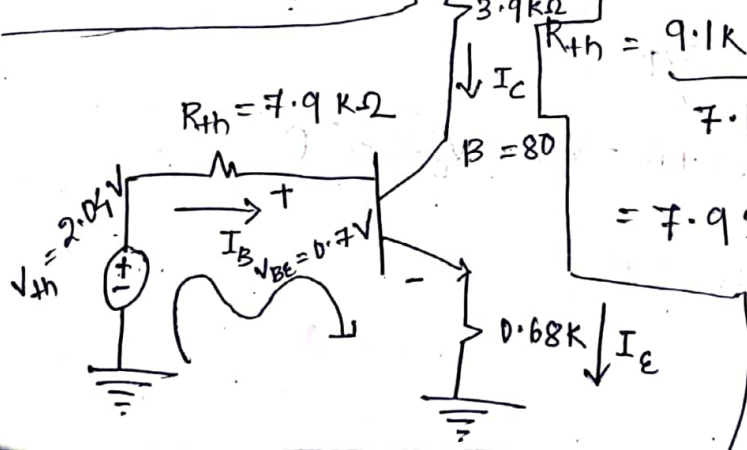


calculation of V_{th} :-

$$V_{th} = \frac{16 \times 9.1K}{62K + 9.1K} = 2.04V$$

Calculation of R_{th} :-

$$R_{th} = \frac{9.1K \times 62K}{7.1K + 62K} = 7.936K\Omega$$



$$2.04 - 7.9K I_B - 0.7 - I_E \times 0.63K = \frac{I_E}{\beta + 1}$$

$$I_E \approx I_C$$

$$I_B = \frac{I_E}{\beta + 1} \approx \frac{I_C}{\beta}$$

$$I_E = \frac{V_E - 0}{0.68K}$$

$$V_E = ?$$

$$V_{BE} = 0.7$$

$$\Rightarrow V_B - V_E = 0.7$$

$$\Rightarrow \boxed{V_B = 0.7 + V_E}$$

Applying KVL at O/P:-

$$16 - I_C \times 3.9K - V_{CE} - I_E \times 0.68K = 0$$

$$V_{CE} = ?$$

$$V_{CE} = V_C - V_E$$

$$\boxed{V_C = V_{CE} + V_E}$$

Q:- Determine α & γ for $\alpha = 0.99$.

$$\alpha = 0.99$$

$$\beta = \frac{\alpha}{1 - \alpha} = \frac{0.99}{1 - 0.99} = 99$$

$$\gamma = \beta + 1$$

$$= 99 + 1 = 100$$

Q:- Determine α & γ for $\beta = 99$.

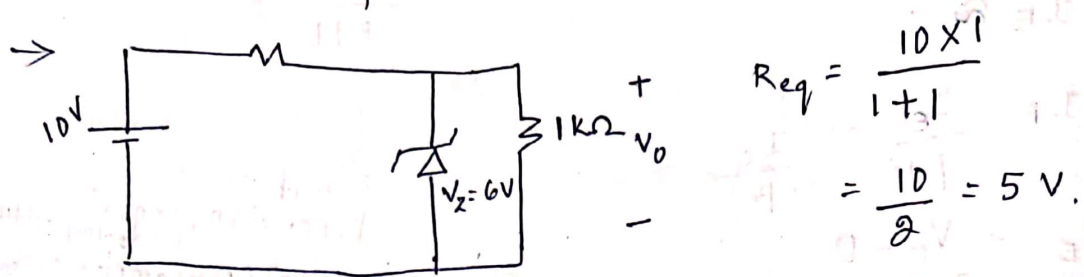
$$\text{Here, } \gamma = 99 + 1 = 100$$

$$\alpha = \frac{\beta}{\beta + 1} = \frac{99}{99 + 1}$$

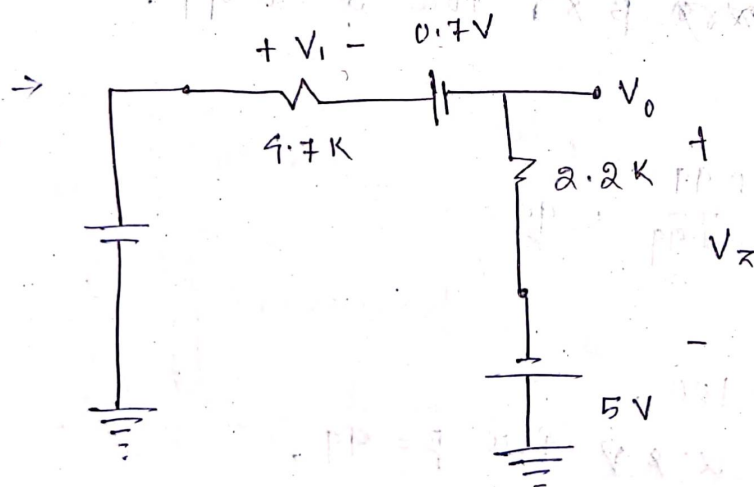
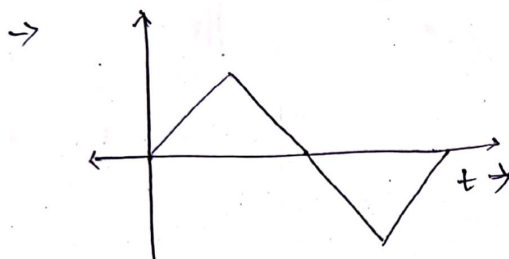
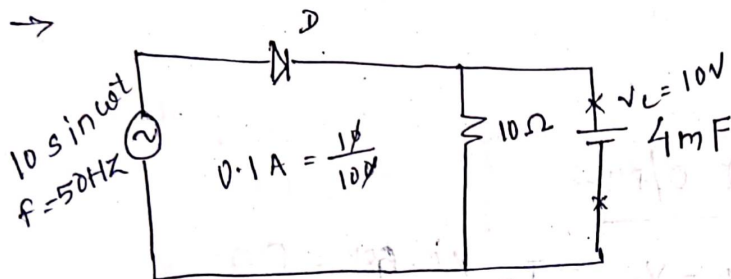
$$= \frac{99}{100} = 0.99$$

BJT :-
operation, constⁿ
(doping, width)
operⁿ (for amplification)
schematic diagram
(npn & pnp)

Solution of mid term exam paper :-



- R_c is used to make the transistor safe.
- R_e is used to make $I_{c dc}$ independent of β or used to produce -ve feedback.



$10 - 4.7KI$

d)

